

# University of Pretoria Yearbook 2017

## Analogue electronic design 732 (EME 732)

<b>Qualification</b>	Postgraduate
<b>Faculty</b>	<a href="#">Faculty of Engineering, Built Environment and Information Technology</a>
<b>Module credits</b>	32.00
<b>Programmes</b>	<a href="#">BEngHons Microelectronic Engineering</a>
<b>Prerequisites</b>	Analogue electronic design EME732 (E5), 3rd year Electronics or equivalent or permission from the lecturer
<b>Contact time</b>	32 contact hours per semester
<b>Language of tuition</b>	Module is presented in English
<b>Academic organisation</b>	Electrical, Electronic and Com
<b>Period of presentation</b>	Semester 1

### Module content

The integrated circuit (IC) or “chip” is the motor of the present electronic revolution. The ever-increasing impact of electronics is driven mainly by large-scale ICs such as processor and memory chips. The electronic circuit techniques used in these chips can only be understood on a deep level by a study of classical analogue electronics aimed at integrated circuit design for fabrication in CMOS, bipolar and BiCMOS processes. In addition, analog circuit techniques perform an essential role in the interfaces between the “real world” and digital systems. Examples are: voltage references, amplifiers, filters, level-converters, buffers. Important topics in this respect are feedback and stability theory as specialized for electronic circuits. The course includes: IC fabrication technology, models for IC transistors, transistor current sources and amplifiers, output stages, operational amplifiers, frequency response and stability of feedback amplifiers, nonlinear and computational circuits.

The information published here is subject to change and may be amended after the publication of this information. The [General Regulations \(G Regulations\)](#) apply to all faculties of the University of Pretoria. It is expected of students to familiarise themselves well with these regulations as well as with the information contained in the [General Rules](#) section. Ignorance concerning these regulations and rules will not be accepted as an excuse for any transgression.